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PATENT AND TRADEMARK OFFICE
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Sheet 1 of 2

ATTORNEY DOCKET No.	SERIAL NO.
MP0071	09/759,151
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Yat-Tung Lam et al	
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U.S. F	PATENT DO	CUMENTS				
Ref. Desig.	Examiner's Initials	Document Number	Date	Name	Class/ Subclass	(If appropriate) Filing Date
1.	Un	3,571,806	03/1971	Makie et al		
2.		4,607,348	08/1986	Sheth, Jayesh V.		
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4.		5,133,060	07/21/1992	Weber et al		
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Sheet 2 of 2

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1.	14/1	Hans-Peter Messmer, "The Indispensable PC Hardware Book", 1995, Addison-Wesley Publishers, Ltd., 2nd Ed. pp. 1072-1077.			
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3.		T. Chelcea, "A Low-Latency FIFO for Mixed-Clock Systems", Department of Computer Science, Columbia University, 45 pages			

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